

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	19	"341"/\$.ccls. and Hirata.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/21 14:24
S2	3	"341"/118-121,144,153,154.ccls. and ((current near3 generat\$3) same (variable near4 resist\$7) same transistor\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/21 16:48
S3	43	"341"/118-121,144,153,154.ccls. and (current near3 generat\$3) and (variable\$1 near4 resist\$7) and transistor\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/21 15:39
S4	37	"341"/\$.ccls. and (current near3 generat\$3) and (variable\$1 near4 resist\$7) and transistor\$1 and (digital adj analog)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/21 14:51
S5	16	S4 not S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/21 14:50
S6	34	"330"/\$.ccls. and (current near3 generat\$3) and (variable\$1 near4 resist\$7) and transistor\$1 and (digital adj analog)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/21 16:42
S7	5	(digital adj analog).ti. and (current near3 generat\$3) and (variable\$1 near4 resist\$7) and transistor\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/21 15:09
S8	553	(digital adj analog) and (current near3 generat\$3) and (variable\$1 near4 resist\$7) and transistor\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/21 15:23
S9	532	S8 not S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/21 15:10

S10	19	(digital adj analog) and (current near3 generat\$3) and (variable\$1 near4 resist\$7) and transistor\$1 and (trim\$6 with current)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/21 15:31
S11	4	("5119095" "5302951" "5668550" "5870049").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/02/21 15:26
S12	7	(digital adj analog) and (current near3 generat\$3) and (variable\$1 near4 resist\$7) and transistor\$1 and (trim\$6 with (DAC or DA or D/A or (dial adj analog)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/21 15:33
S13	130	"341"/118-121,143,144,153,154.ccls. and (variable\$1 near4 resist\$7) and transistor\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/21 15:40
S14	9705467	S13 no S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/21 15:40
S15	87	S13 not S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/21 15:40
S16	9	"341"/118-121,136,144,153,154.ccls. and ((current near3 generat\$3) same (parallel with resist\$7) same transistor\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/21 16:40
S17	39	"330"/\$.ccls. and (current near3 generat\$3) and (parallel near4 resist\$7) and transistor\$1 and (digital adj analog)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/21 16:43
S18	114	"341"/118-121,144,153,154.ccls. and ((current near3 generat\$3) same (resist\$7) same transistor\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/21 16:48
S19	104	S18 not S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/21 16:49

S20	58	(S18 not S3) and weigh\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/21 17:11
S21	24	341/144,153,154.ccls. and (current near3 segment\$3) and transistor and (resist\$7 with switch\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/21 17:31
S22	13	341/118-121.ccls. and (current near3 segment\$3) and transistor and (resist\$7 with switch\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/21 17:39
S23	0	current near generat\$3.ti. and transistor and (trim\$4 with resistor\$1) and (digital adj analog)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/21 17:41
S24	23	current.ti. and transistor and (trim\$4 with resistor\$1) and (digital adj analog)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/21 17:43
S25	6337	(two with spare with cell\$1 with (one adj bit) or ('1' adj bit) with stor\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/22 09:57
S26	14	(two with spare with cell\$1 with ((one adj bit) or ('1' adj bit)) with stor\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/22 10:33
S27	58	"341"/\$.ccls. and fold\$3.ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/22 17:17
S28	35	"341"/\$.ccls. and ((current near source\$1) with parallel with resist\$7) and (digital adj analog)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/22 17:36
S29	10	"330"/\$.ccls. and ((current near source\$1) with parallel with resist\$7) and (digital adj analog)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/22 17:40

S30	130	((current near source\$1) with parallel with resist\$7) and (digital adj analog)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/22 17:57
S31	95	S30 not S28	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/22 17:41
S32	86	S31 not S29	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/22 17:41
S33	52	(current with generat\$3 with parallel with resist\$7) and (digital adj analog)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/22 18:01
S34	42	(current with generat\$3 with parallel with resist\$7) and (digital adj analog) and transistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/22 18:12
S35	99	current.tr. and (parallel with resist\$7) and (digital adj analog) and transistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/22 18:12
S36	2	"20040257058".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/08 18:40
S37	522	"341"/\$.ccls. and (switch\$3 with cmos)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/08 19:25
S38	105	"341"/144\$.ccls. and (switch\$3 with cmos)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/08 19:25
S39	6725	((current near source) with resist\$8) and bipolar	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/08 20:32

S40	5	((current near source) with (resist\$8 near network) with (bipolar near transistor))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/08 21:27
S41	4	(transistor with (resist\$8 near network) with ((digital adj analog) or adc or 'a/d'))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/08 21:46
S42	1422	(control with resist\$7 with ((digital adj analog) or adc or 'a/d'))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/08 21:46
S43	141	(transistor with control\$4 with resist\$7 with ((digital adj analog) or adc or 'a/d'))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/08 21:47
S44	2	"330"/\$.ccls. and (transistor with control\$4 with resist\$7 with ((digital adj analog) or adc or 'a/d'))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/08 21:47
S45	5	"330"/\$.ccls. and (transistor with control\$4 with resist\$7 with ((digital adj analog) or dac or 'd/a'))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/08 21:48
S46	171	341/154.ccls. and (switch\$3 with ladder)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/09 12:35
S47	125	341/144-154.ccls. and (switch\$3 with nmos)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 09:58
S48	0	"365"/\$.ccls. and TCAM and (cascod\$3 with mos)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 09:59
S49	6	"365"/\$.ccls. and TCAM and mos	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 10:01